# EAST Search History

| L29 | 2 | (((((sav$4 or conserv$5 or low) near2 (power or energy))near2 (entr$4 adj2(state or mode))))with ((network or interconnect)near3 (activit$3 or traffic or signal))) same (inform$5 or packet or message or (idle near3 (set or packet))) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/09/21 11:57 |
|-----|---|---|---|---|---|---|
| L30 | 8 | (((((sav$4 or conserv$5 or low) near2 (power or energy))adj2 (entry adj(state or mode)))) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/09/21 12:08 |

# EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L22 | 2 | ("6463542").URPN. | USPAT | OR | ON | 2006/09/21 10:17 |
| L23 | 767 | ((enter$4 or mov$3 or switch$4)near3 (((sav$4 or conserv$5 or low) near2 (power or energy))adj2 (state or mode)))with (inform$5 or packet or message or (idle near3 (set or packet))) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/09/21 10:38 |
| L24 | 19 | l23 and ((bypass$4 or overrid$3 or leav$4)near3 (((sav$4 or conserv$5 or low) near2 (power or energy))adj2 (state or mode))) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/09/21 11:53 |
| L25 | 6 | ((enter$4 or mov$3 or switch$4)near3 (((sav$4 or conserv$5 or low) near2 (power or energy))adj2 (state or mode)))with (inform$5 or packet or message or (idle near3 (set or packet)))same (activity with (interconnect or network)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/09/21 11:48 |
| L26 | 6 | l23 same (activity with (interconnect or network)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/09/21 10:36 |
| L27 | 36 | (((((sav$4 or conserv$5 or low) near2 (power or energy))adj2 (state or mode)))with ((network or interconnect)near3 (activit$3 or traffic or signal))) same (inform$5 or packet or message or (idle near3 (set or packet))) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/09/21 12:07 |
| L28 | 3 | l27 and ((bypass$4 or overrid$3 or leav$4)near3 (((sav$4 or conserv$5 or low) near2 (power or energy))adj2 (state or mode))) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/09/21 11:55 |

C:\Documents and Settings\npatel3\My Documents\EAST\Workspaces\10749619.wsp

PALM INTRANET

## Inventor Name Search Result

Your Search was:

Last Name = PUFFER
First Name = DAVID

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|---|---|---|---|---|---|
| 08625066 | 6125425 | 150 | 03/29/1996 | MEMORY CONTROLLER PERFORMING A MID TRANSACTION REFRESH AND HANDLING A SUSPEND SIGNAL | PUFFER, DAVID |
| 10284596 | 7120765 | 150 | 10/30/2002 | MEMORY TRANSACTION ORDERING | PUFFER, DAVID |
| 11156838 | Not Issued | 30 | 06/20/2005 | Peripheral interface alert message for downstream device | PUFFER, DAVID |
| 09595509 | 6734862 | 150 | 06/14/2000 | MEMORY CONTROLLER HUB | PUFFER, DAVID M. |
| 09644967 | 7116331 | 150 | 08/23/2000 | MEMORY CONTROLLER HUB INTERFACE | PUFFER, DAVID M. |
| 10421204 | 6871119 | 150 | 04/22/2003 | FILTER BASED THROTTLING | PUFFER, DAVID M. |
| 10749619 | Not Issued | 71 | 12/30/2003 | Optimizing exit latency from an active power management state | PUFFER, DAVID M. |
| 10749721 | Not Issued | 30 | 12/31/2003 | Lane to lane deskewing via non-data symbol processing for a serial point to point link | PUFFER, DAVID M. |
| 10750013 | Not Issued | 161 | 12/31/2003 | Buffer management via non-data symbol processing for a point to point link | PUFFER, DAVID M. |
| 10750081 | Not Issued | 30 | 12/31/2003 | Receiver symbol alignment for a serial point to point link | PUFFER, DAVID M. |
| 11174662 | Not Issued | 61 | 07/06/2005 | Memory controller hub interface | PUFFER, DAVID M. |
| 08581063 | Not Issued | 161 | 12/29/1995 | METHOD FOR UTILIZING A SINGLE MULTIPLEX ADDRESS BUS BETWEEN DRAM, SRAM, AND ROM | PUFFER, DAVID M. |
| 08726700 | 5901298 | 150 | 10/07/1996 | METHOD FOR UTILIZING A SINGLE MULTIPLEX ADDRESS BUS BETWEEN DRAM, SRAM, AND ROM | PUFFER, DAVID M. |

## Inventor Search Completed: No Records to Display.

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| Last Name | First Name | |
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| PUFFER | DAVID | Search |

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## PALM INTRANET

## Inventor Name Search Result

Your Search was:

Last Name = MITBANDER
First Name = SUNEEL

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|---|---|---|---|---|---|
| 10749619 | Not Issued | 71 | 12/30/2003 | Optimizing exit latency from an active power management state | MITBANDER, SUNEEL G. |
| 10749721 | Not Issued | 30 | 12/31/2003 | Lane to lane deskewing via non-data symbol processing for a serial point to point link | MITBANDER, SUNEEL G. |
| 10750034 | Not Issued | 20 | 12/31/2003 | Programmable measurement mode for a serial point to point link | MITBANDER, SUNEEL G. |
| 10750081 | Not Issued | 30 | 12/31/2003 | Receiver symbol alignment for a serial point to point link | MITBANDER, SUNEEL G. |

## Inventor Search Completed: No Records to Display.

| | Last Name | First Name | |
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## Inventor Name Search Result

Your Search was:

Last Name = KOTAMREDDY
First Name = SARATH

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|---|---|---|---|---|---|
| 10749429 | Not Issued | 30 | 12/31/2003 | Automatic impedance matching compensation for a serial point to point link | KOTAMREDDY, SARATH |
| 10749721 | Not Issued | 30 | 12/31/2003 | Lane to lane deskewing via non-data symbol processing for a serial point to point link | KOTAMREDDY, SARATH |
| 10750013 | Not Issued | 161 | 12/31/2003 | Buffer management via non-data symbol processing for a point to point link | KOTAMREDDY, SARATH |
| 10750081 | Not Issued | 30 | 12/31/2003 | Receiver symbol alignment for a serial point to point link | KOTAMREDDY, SARATH |
| 10795037 | Not Issued | 41 | 03/04/2004 | Apparatus and method for open loop buffer allocation | KOTAMREDDY, SARATH |
| 10795939 | Not Issued | 71 | 03/08/2004 | Queue partitioning mechanism | KOTAMREDDY, SARATH |
| 10749619 | Not Issued | 71 | 12/30/2003 | Optimizing exit latency from an active power management state | KOTAMREDDY, SARATH K. |

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| KOTAMREDDY | SARATH | Search |

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